

An L-Band Ultra-Low-Power-Consumption Monolithic Low-Noise Amplifier

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A low-power-consumption variable-gain low-noise amplifier (LNA) is demonstrated. To achieve low noise, low distortion, and low power consumption simultaneously, a cascode connection between an enhancement-mode GaAs MESFET (EFET) and a depletion-mode GaAs MESFET (DFET) is employed. The EFET is superior to the DFET in its gain and noise figure performance while the DFET offers good intermodulation distortion performance. The advantages of both types of FET's are combined in the developed LNA. It shows excellent performance with an NF of 2.0 dB, a gain of 12.2 dB, and an IP/sub 3/ of 5.1 dBm at 1.9 GHz. The demonstrated performance satisfies the specifications of the Japanese Personal Handy phone System even at the ultra-low power consumption of 2.0 mW.

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